

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Method To Reduce Stacking Fault Nucleation Sites And Reduce Forward Voltage Drift In Bipolar Devices	
Application Number :		
Date :		
First Named Applicant:	Dr. Joseph J. Sumakeris	
Confirmation Number:		
Attorney Docket Number:	5000.319	
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>		
Submitted By:		Elec. Sign.
Philip Summa Registered Number: 31573		/Philip Summa/ Sign. Capacity Attorney

Documents being submitted:	Files
us-request	5000319-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	5000319-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	xmlspec319-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd figures1and4.tif figures2and3.tif figures5and6.tif
Comments	